

Figure 2: Sample Flow Diagram for a Data Packet

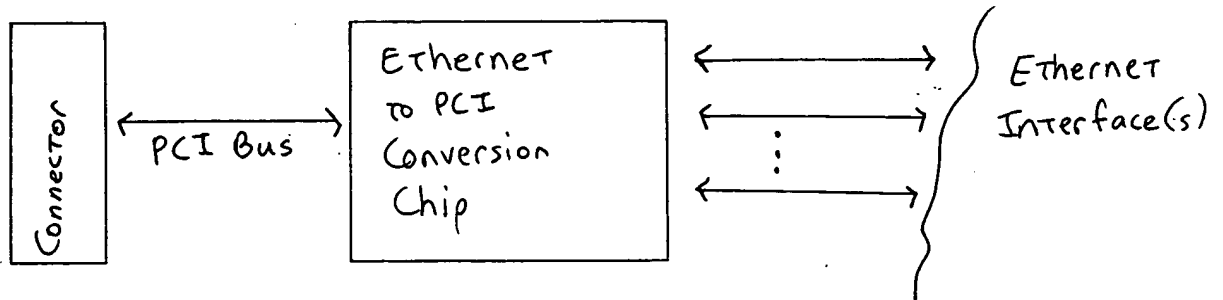


Figure 3a: Port Adaptor Design for an Ethernet Interface

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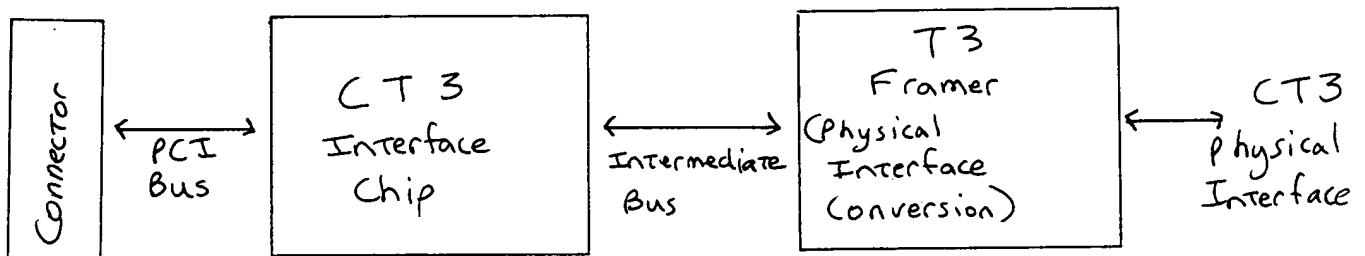


Figure 3b: Port Adaptor Design for a Channelized T3 Line (CT3) Interface

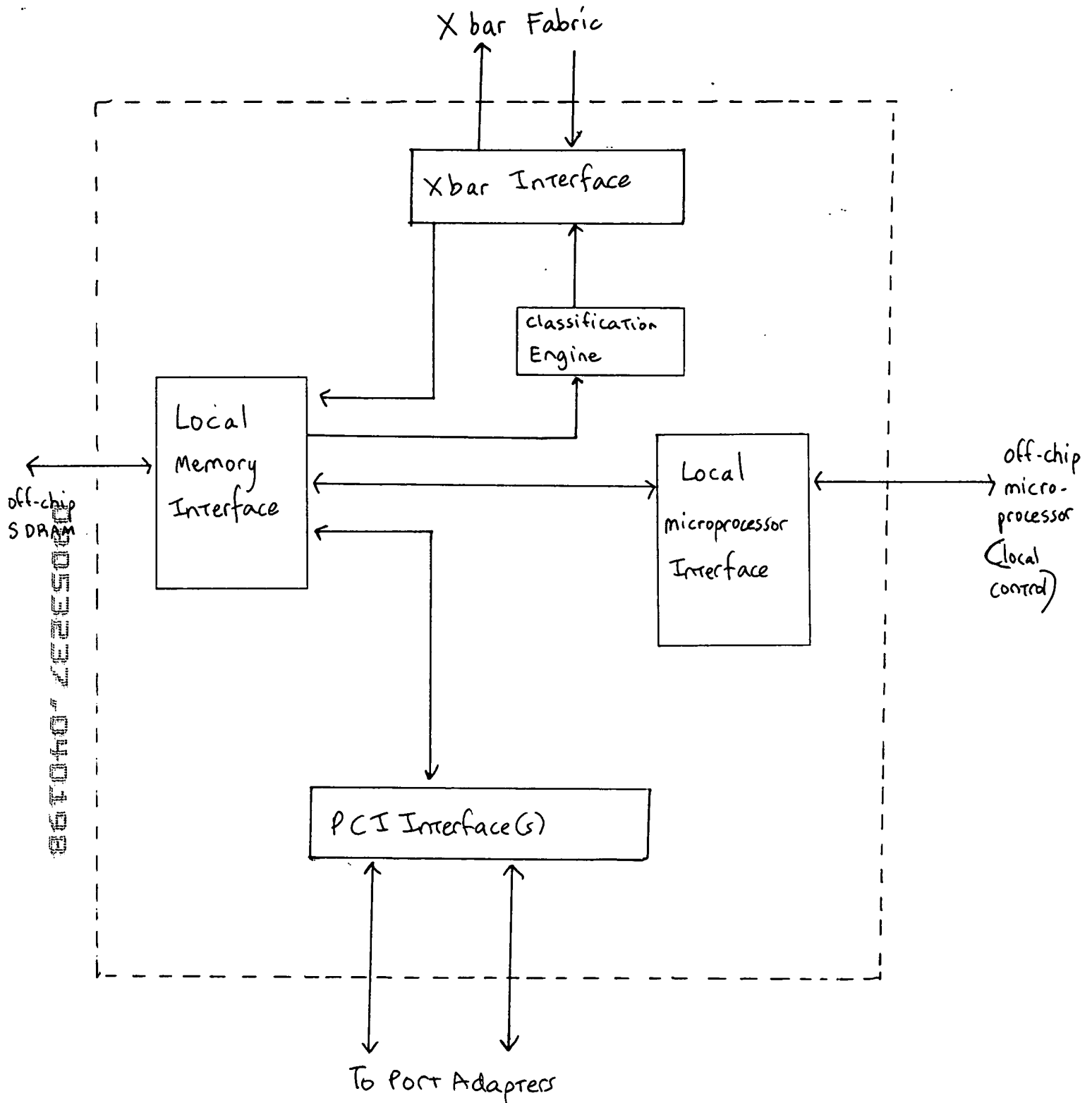


Figure 4: Data Compiler